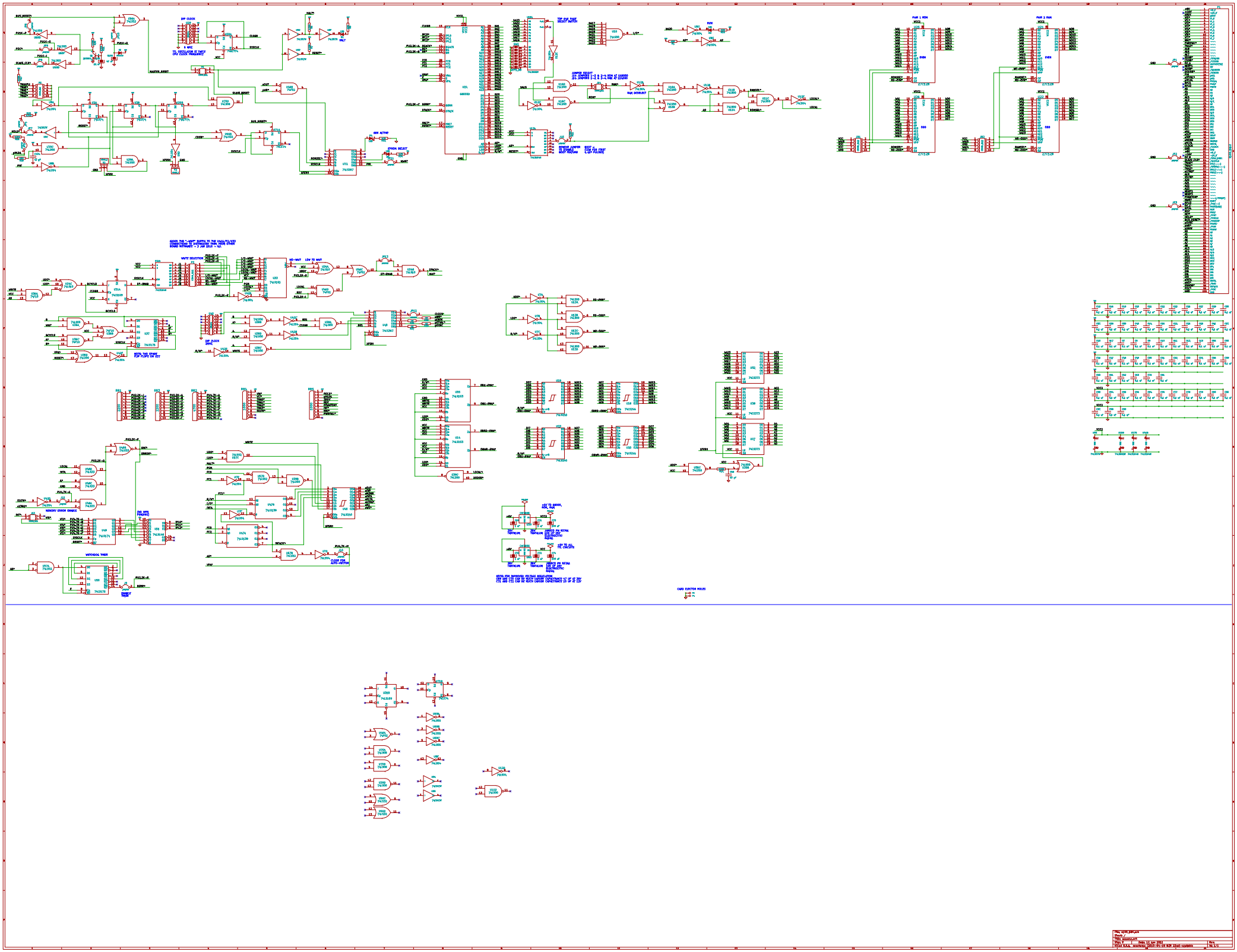
DIP CLOCK
8 MHZ
TO OSCILLATOR IF PARTS ARE CLOCK TOGGLE
MASTER RESET
SLAVE RESET
BUS RESET
BUS ACTION
EPROM DESELECT
TOP RAM PAGE SELECT MATRIX
ADDRESS SELECT
ALL ADDRESSES 1-1-1-1-1-1 ROM AT ADDRESS ALL ADDRESSES 0-0-0-0-0-0 RAM AT ADDRESS
RAM DESELECT
MEMORY DUMPER DISABLE DEVICE
BUSY IS FOR THE CPU TO WAIT FOR THE PAGE
PAGE 1 ROM
PAGE 2 RAM
WAIT SELECTION
WE-WAIT LOW TO WAIT
ROMS TWO WAIT BUSY0 TO TWO WAIT TO 1533 (TRANSFERRED TO AUTOMATICALLY TWO WAIT ROMS BUSY0 BETWEEN -3 FOR 2532 - N1
ROMS TWO WAIT BUSY0 TO TWO WAIT TO 1533 (TRANSFERRED TO AUTOMATICALLY TWO WAIT ROMS BUSY0 BETWEEN -3 FOR 2532 - N1
BUSY0 TWO WAIT BUSY0 TO TWO WAIT TO 1533 (TRANSFERRED TO AUTOMATICALLY TWO WAIT ROMS BUSY0 BETWEEN -3 FOR 2532 - N1
BUSY0 TWO WAIT BUSY0 TO TWO WAIT TO 1533 (TRANSFERRED TO AUTOMATICALLY TWO WAIT ROMS BUSY0 BETWEEN -3 FOR 2532 - N1
DIP CLOCK
MEMORY ERROR CHECK
NO WAIT PROMPTS
WATCHDOG TIMER
CLEAR FOR WAIT-WATCHDOG
CARD EJECTOR PULLS
VCC2
VCC3
VCC4
VCC5
VCC6
VCC7
VCC8
VCC9
VCC10
VCC11
VCC12
VCC13
VCC14
VCC15
VCC16
VCC17
VCC18
VCC19
VCC20
VCC21
VCC22
VCC23
VCC24
VCC25
VCC26
VCC27
VCC28
VCC29
VCC30
VCC31
VCC32
VCC33
VCC34
VCC35
VCC36
VCC37
VCC38
VCC39
VCC40
VCC41