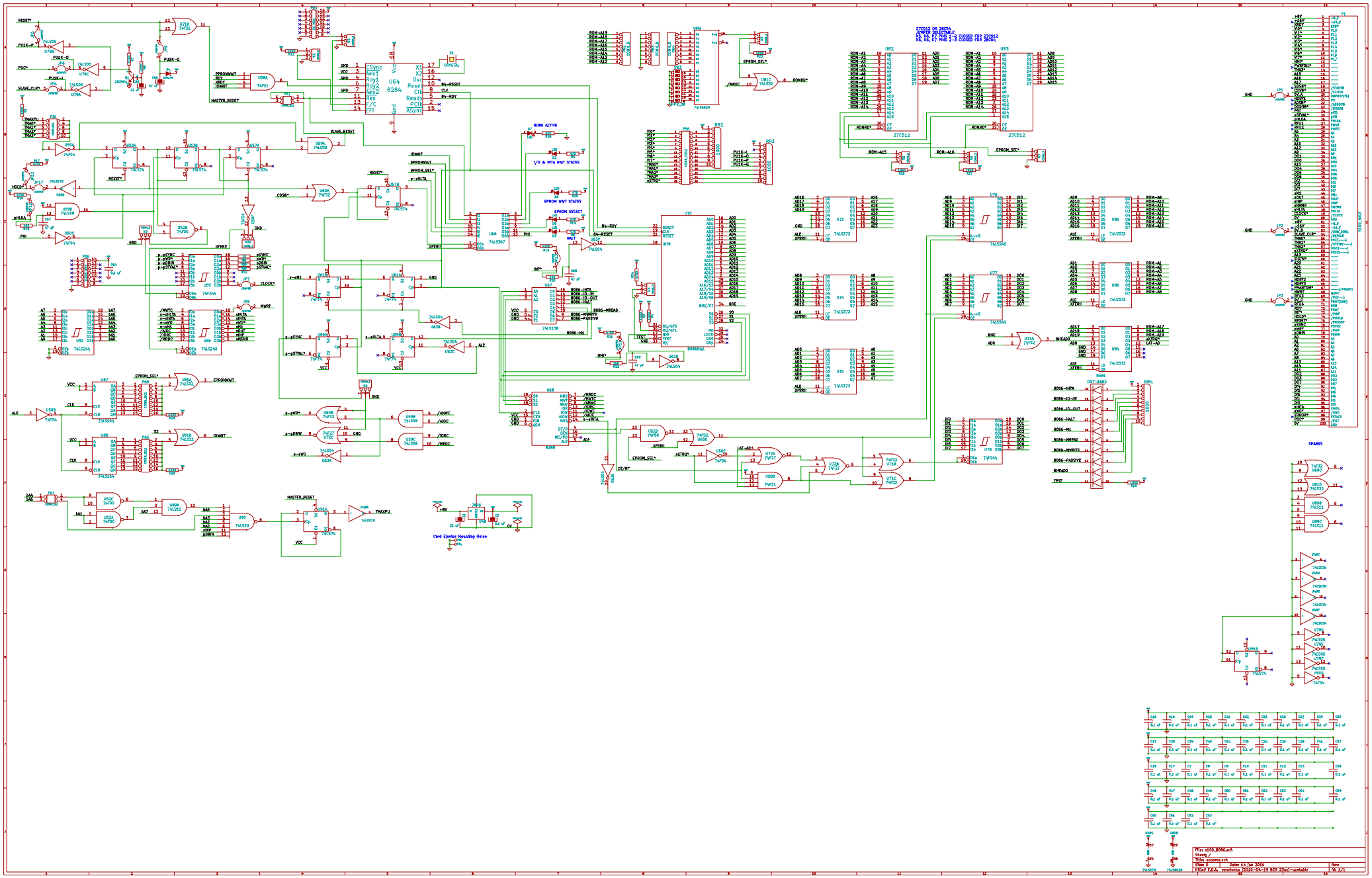
27C512 OR 27C64
JUMPER SELECTABLE
K5, K6, K7 PINS 1-2 CLOSED FOR 27C512
K5, K6, K7 PINS 2-3 CLOSED FOR 27C64
8086 ACTIVE
I/O & INTA WAIT STATES
EPROM WAIT STATES
EPROM SELECT
Card Ejector Mounting Holes
SPARES
File: s100_8086.sch
Sheet: /
Title: sssmsm.sch
Size: B
Date: 14 Jun 2011
Rev:
KiCad E.D.A. eeschema (2010-04-19 BZR 2960)-unstable
Id: 1/1