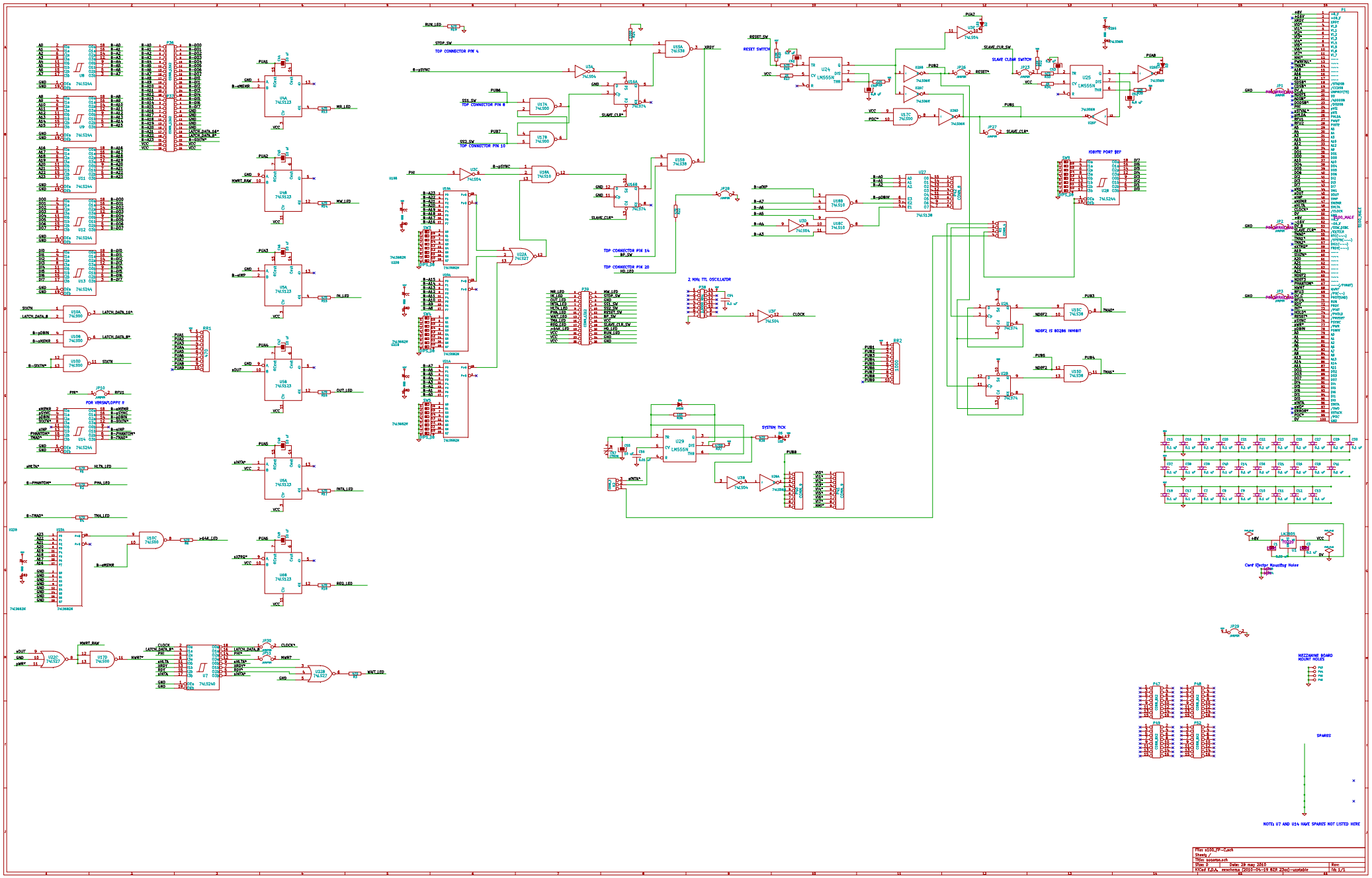
RUN LED
STOP_SW
TOP CONNECTOR PIN 4
B-pSYNC
SS1_SW
TOP CONNECTOR PIN 8
SS2_SW
TOP CONNECTOR PIN 10
PHI
B-pDSYNC
TOP CONNECTOR PIN 14
BP_SW
TOP CONNECTOR PIN 20
HD_LED
RESET SW
RESET SWITCH
SLAVE CLR SW
SLAVE CLEAR SWITCH
RESET*
SLAVE_CLR*
IOBYTE PORT DEF
2 MHz TTL OSCILLATOR
CLOCK
SYSTEM TICK
NOBF2 IS BC286 INVERT
MR LED
MW LED
TN LED
OUT LED
HLTA LED
PHA LED
TMA LED
+5A LED
REQ LED
WWT LED
LATCH_DATA_16*
LATCH_DATA_8*
STATN
RFU5
POR VERSA/LOPPY 9
CLOCK*
MWR*
MEZZANINE BOARD MOUNT HOLES
SPARES
NOTE: U7 AND U14 HAVE SPARES NOT LISTED HERE
File: s100_ZP-Cash
Sheet: /
Title: sosemuk.sch
Size: B
Date: 29 may 2010
Rev:
KiCad E.D.A. eeschema (2010-04-19 BZR 2960)-unstable
Id: 1/1