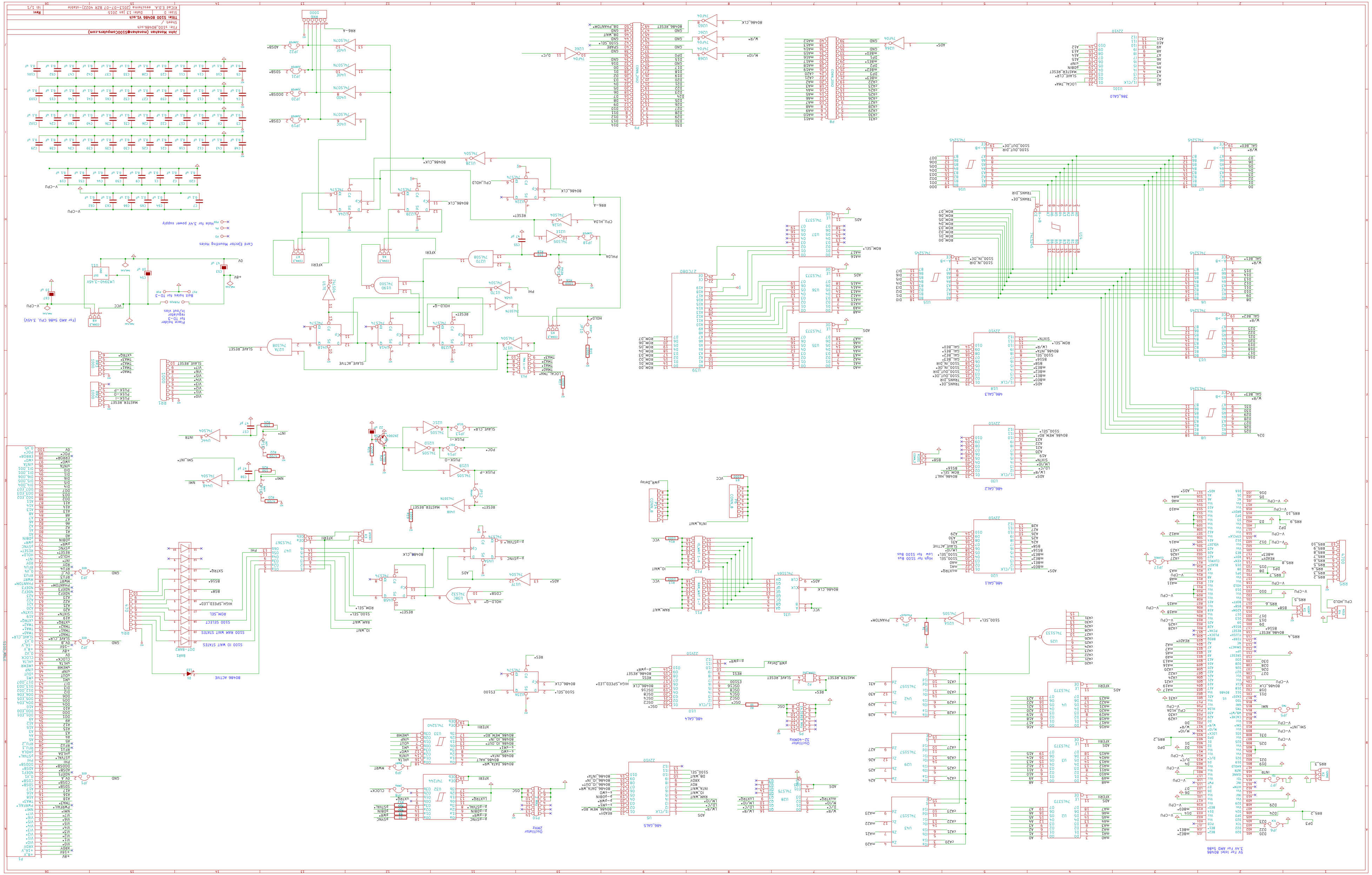
John Monahan (monahan@S100Computers.com)
File: S100_8086.sch
Title: S100 8086 V1.sch
Size: D
Date: 13 Jan 2015
Rev:
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable
Id: 1/1
S100 8086 ACTIVE
S100 I/O WAIT STATES
S100 RAM WAIT STATES
S100 SELECT
ROM_SEL
HIGH_SPEED_LED
Card Ejector Mounting Holes
Place holder for 10-3 regulator in/out vias
Bolt holes for TO-3
For AMD 5x86 CPU, 3.45V)
5V For Intel 8086
3.4V For AMD 5x86