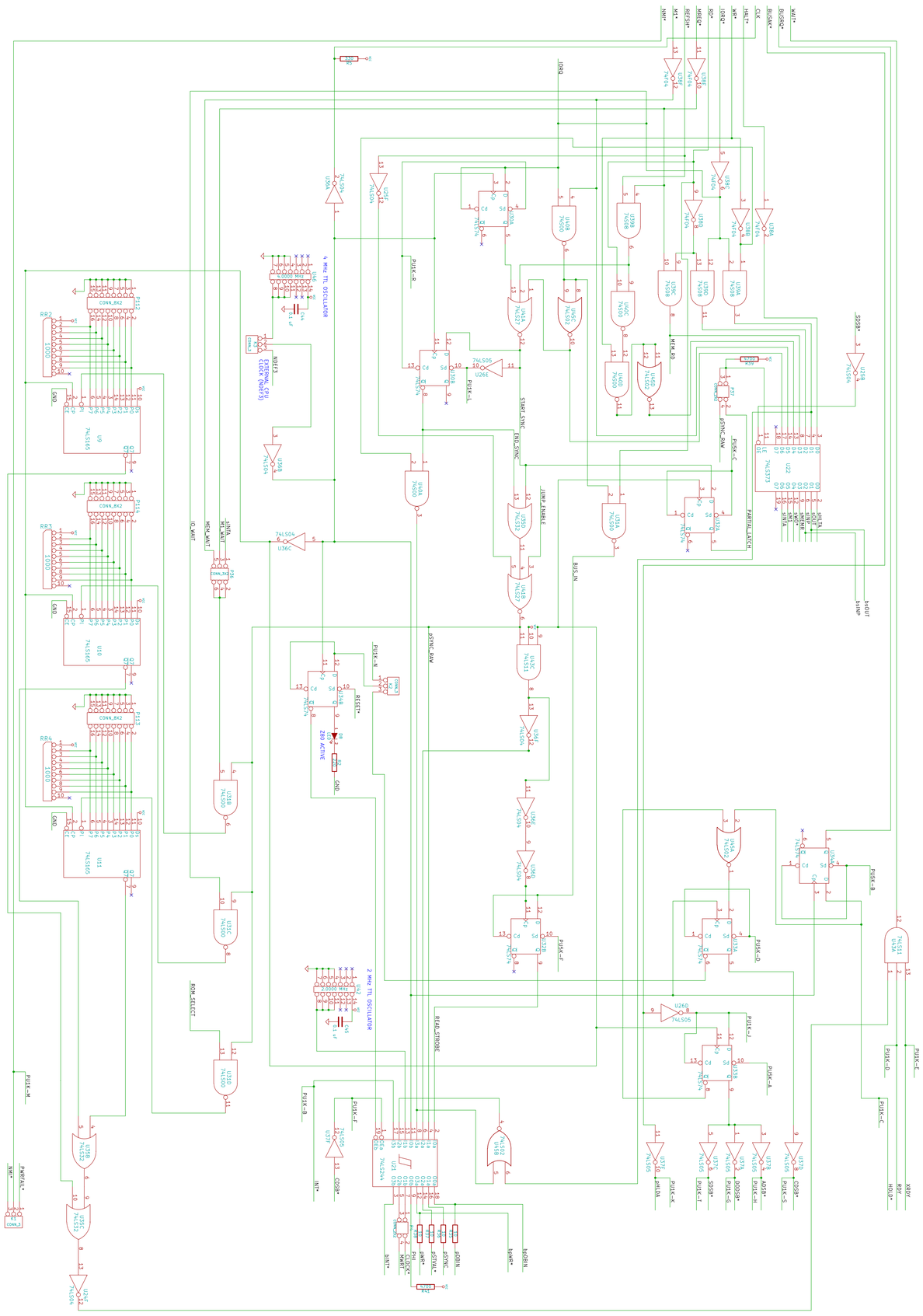
4 MHz TTL OSCILLATOR
EXTERNAL CPU CLOCK (NOT#3)
2 MHz TTL OSCILLATOR
START_SYNC
END_SYNC
JUMP_ENABLE
PARTIAL_LATCH
READ_STROBE
ROM_SELECT
BUS_IN
bDOUT
bINP
bDBIN
bSYNC
bWR*
bDBIN*
bINT*
bWAIT*
bHLDA
bRESET*
bSTVAL*
bSTSTB*
bMWRT
bPWR*
bPDBIN
bXRDY
bRDY
bHOLD
bSIXTN*
bPHANTOM*
bMEMR*
bINTA*
bM1*
bOUT*
bINP*
bMEMW*
bIOWR*
bIORD*
bIOWR
bIORD
bRESET
bSDSB*
bCDSB*
bODSB*
bADSB*
bDODSB*
bDIDSB*
bCSDSB*
bSSDSB*
bMWRT
bSTVAL
bSTSTB
bPWR
bPDBIN
bSYNC
bDBIN
bHLDA
bWAIT
bINTA
bM1
bOUT
bINP
bMEMR
bINTA
bWR
bRESET
bHLTA
bOUT
bM1
bINP
bMEMW
bIOWR
bIORD
bMEMR
bHLTA
bOUT
bM1
bINP
bMEMW
bIOWR
bIORD
bMWRT
bPWR
bPDBIN
bSYNC
bDBIN
bHLDA
bWAIT
bINTA
bSIXTN
bPHANTOM
bMEMR
bINTA
bM1
bOUT
bINP
bMEMW
bIOWR
bIORD
bXRDY
bRDY
bHOLD
bSSTVAL
bSSTSTB
bSPWR
bSPDBIN
bSSYNC
bSDBIN
bSHLDA
bSWAIT
bSINTA
bSM1
bSOUT
bSINP
bSMEMW
bSIOWR
bSIORD
bSXRDY
bSRDY
bSHOLD
bSSIXTN
bSPHANTOM
bSMEMR
bSINTA
bSM1
bSOUT
bSINP
bSMEMW
bSIOWR
bSIORD
bSMWRT
bSPWR
bSPDBIN
bSSYNC
bSDBIN
bSHLDA
bSWAIT
bSINTA
bSM1
bSOUT
bSINP
bSMEMW
bSIOWR
bSIORD
bSXRDY
bSRDY
bSHOLD
bSSIXTN
bSPHANTOM
bSMEMR
bSINTA
bSM1
bSOUT
bSINP
bSMEMW
bSIOWR
bSIORD
Next version:
No need for P8,9,10
Use 74LS244 as buffer output to inactivate ports
Pullup on JP10
ADSB*
A8
A9
A10
A11
A12
A13
A14
A15
A8
A9
A10
A11
A12
A13
A14
A15
DODSB*
D0
D1
D2
D3
D4
D5
D6
D7
D0
D1
D2
D3
D4
D5
D6
D7
BASE_PORT(A4-7)*
S100_WR*
BOARD_RESET*
D102 LED
D104 LED
RED/GREEN LED
PU470-1
PU470-2
PU470-3
PU470-4
PU470_8
PU470-7
PU470-6
PU470-5
PU470-8
PU1K<A
PU1K<B
PU1K<C
PU1K<D
PU1K<E
PU1K<F
PU1K<H
PU1K<J
PU1K<K
PU1K<L
PU1K<M
PU1K<N
PU1K<P
PU1K<R
PU1K<S
PU1K<T
PU1K<U
PU1K<V
PU1K<W
PU1K<X
PU1K<Y
PU1K<Z
PU1K<AA
PU1K<BB
PU1K<CC
PU1K<DD
PU1K<EE
PU1K<FF
PU1K<HH
PU1K<JJ
PU1K<KK
PU1K<LL
PU1K<MM
PU1K<NN
PU1K<PP
PU1K<RR
PU1K<SS
PU1K<TT
PU1K<UU
PU1K<VV
PU1K<WW
PU1K<XX
PU1K<YY
PU1K<ZZ
PU1K<AAA
PU1K<BBB
PU1K<CCC
PU1K<DDD
PU1K<EEE
PU1K<FFF
PU1K<HHH
PU1K<JJJ
PU1K<KKK
PU1K<LLL
PU1K<MMM
PU1K<NNN
PU1K<PPP
PU1K<RRR
PU1K<SSS
PU1K<TTT
PU1K<UUU
PU1K<VVV
PU1K<WWW
PU1K<XXX
PU1K<YYY
PU1K<ZZZ
SWITCH*
PHANTOM*
EXTIO*
MEMR*
INTA*
M1*
OUT*
INP*
MEMW*
IOWR*
IORD*
ONBOARD ROM ENABLE (INSTALL JUMPER)
EXTERNAL ROM ENABLE (NO JUMPER)
ROM_SELECT*
JUMP_ENABLE
128K RAM (use 64kx2)
U111
628128
MREQ*
ROM_SELECT
RAM_SELECT
U113
27C64
U112
27C256
U1
U2
U3
74LS373
74LS165
U9
U10
U11
74LS244
U19
U20
U101
U107
74LS245
U18
U21
U22
74LS374
U4
U5
U6
U7
U8
74LS157
U12
74LS257
U13
74LS158
U14
74LS139
U15
74LS138
U16
74LS259
U17
74LS670
U106
8255
8255_SEL*
BOARD_RESET*
IOBYTE PORT
USB PORT SELECT
DU-USB232SN
Note Chip is Optional
Pin 1 on Header RTS
SW101
SW102
SW103
SW104
RR101
RR102
RR103
RR104
1K
680
P1
S100_MALE
P2
P3
P4
P5
P6
P7
P8
P9
P10
P11
P12
P13
P14
P15
P16
P17
P18
P19
P20
P21
P22
CONN_22X2
CONN_20X2
CONN_3X2
CONN_2X2
CONN_8X2
CONN_4
CONN_3
CONN_2
Card Ejector Mounting Holes
Pololu 5V 2.5A Regulator
+8V
+5V
-5V
-16.5V
+12V
VCC
GND
C101
C102
C103
C104
C105
C106
C107
C108
C109
C110
C111
C112
C113
C114
C115
C116
C117
C118
C119
C120
C121
C122
C123
C124
C125
C126
C127
C128
C129
C130
C131
C132
C133
C134
C135
C136
C137
C138
C139
C140